

FIG. 1

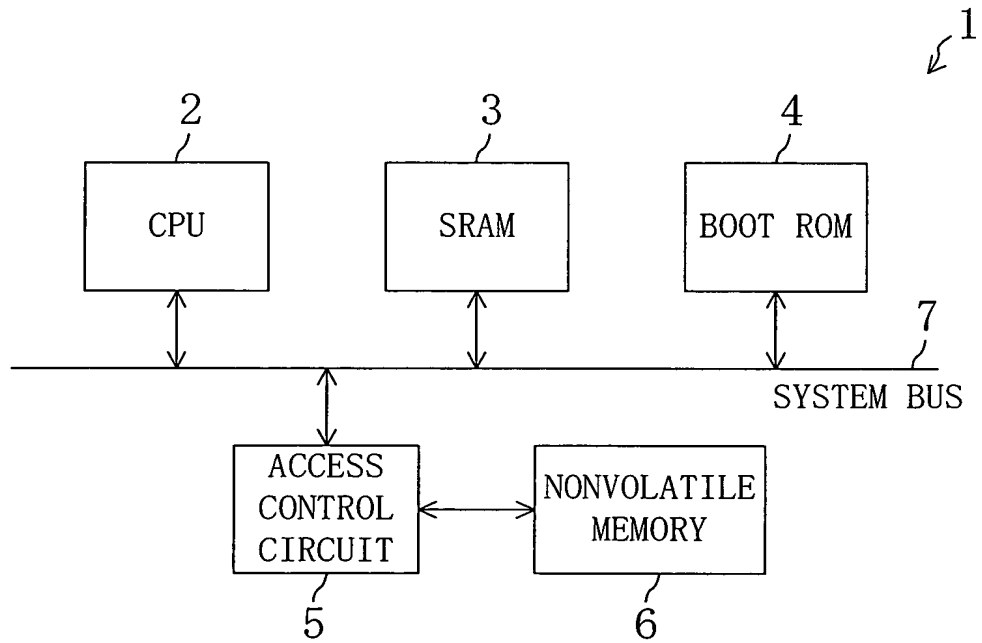


FIG. 2

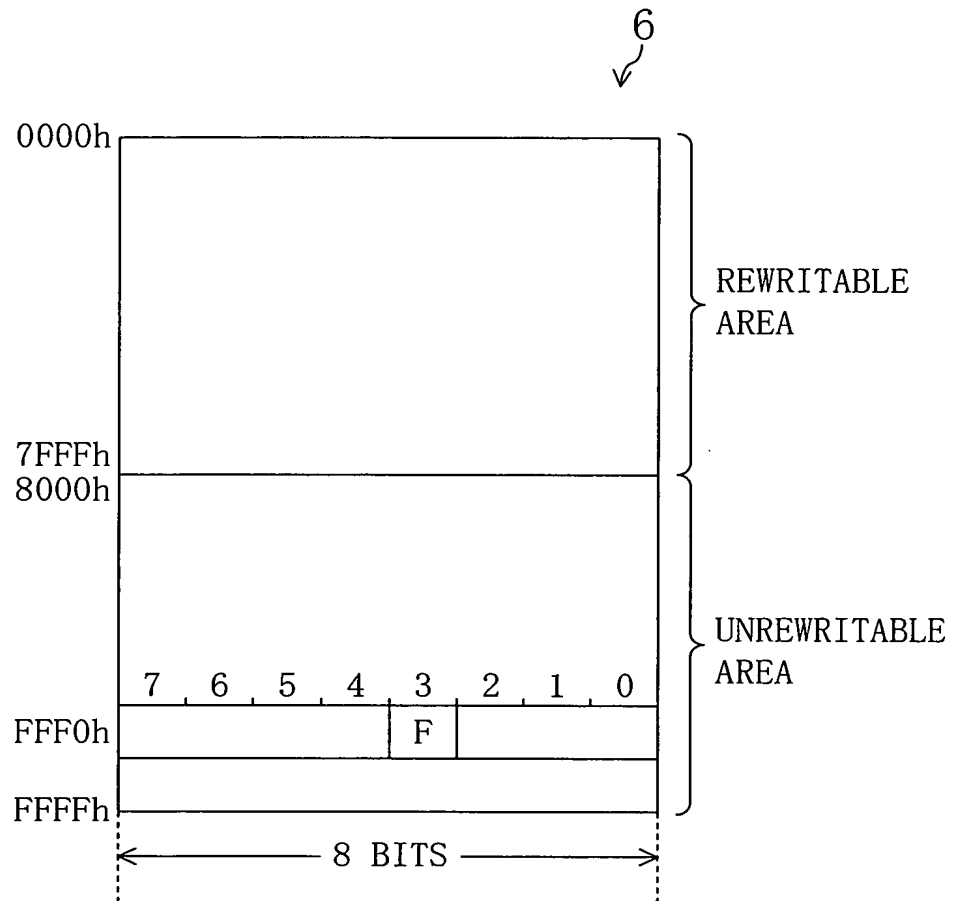


FIG. 3

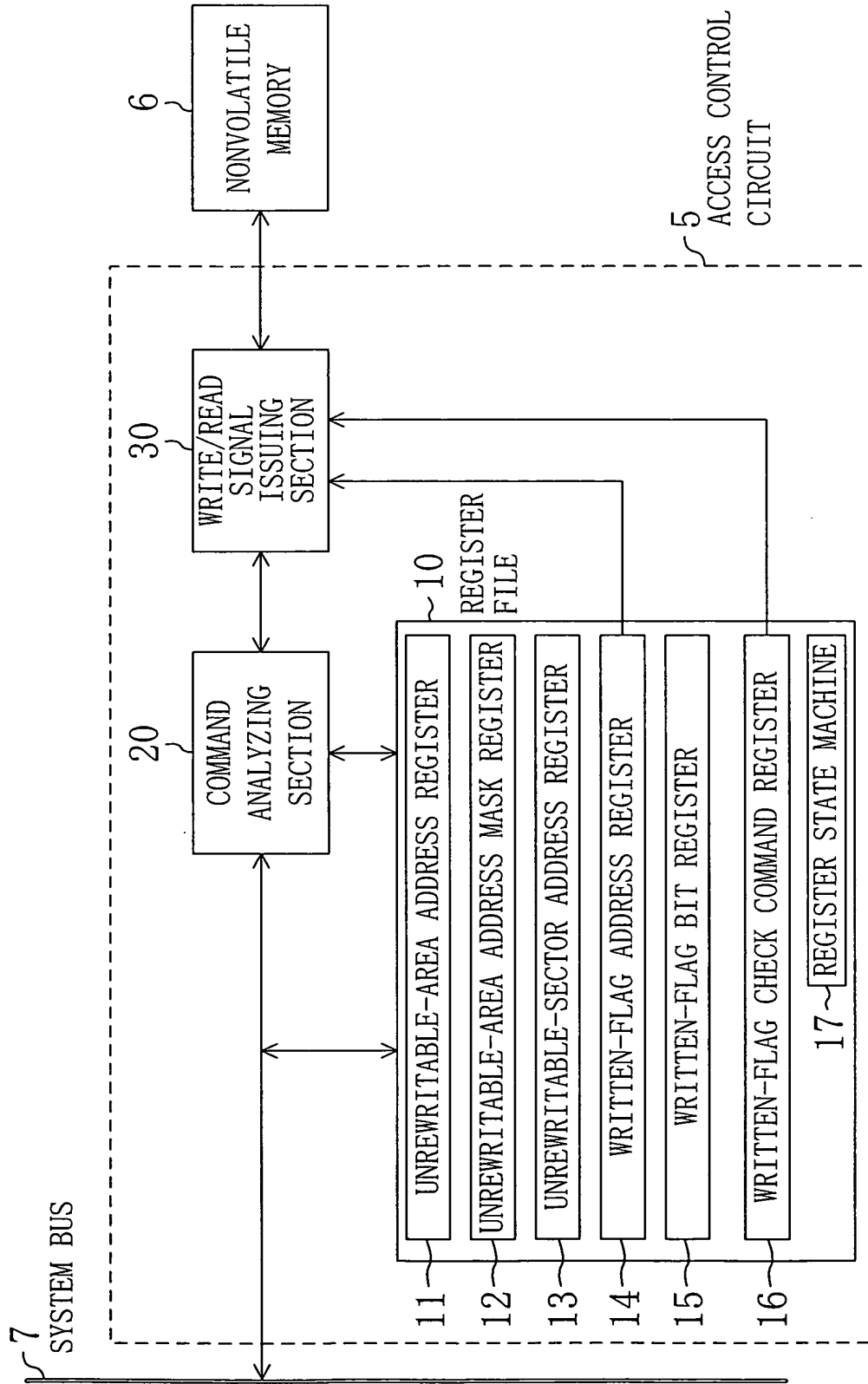


FIG. 4

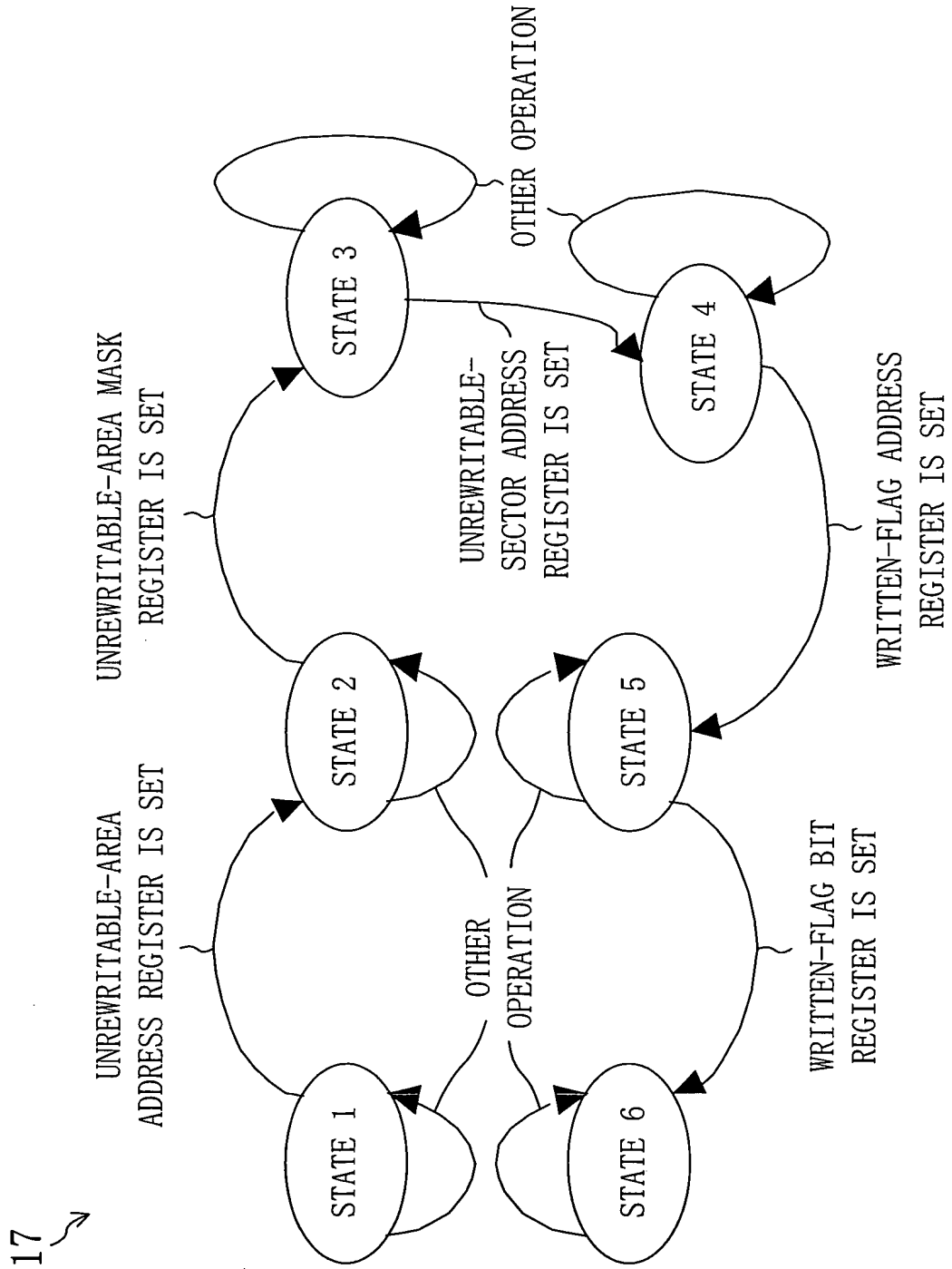


FIG. 5

CYCLE		1	2	3	4	5	6
WRITE	ADRS	555h	2AAh	555h	WA	-	-
	DATA	AAh	55h	A0h	WD	-	-
SECTOR ERASE	ADRS	555h	2AAh	555h	555h	2AAh	SA
	DATA	AAh	55h	80h	AAh	55h	30h
CHIP ERASE	ADRS	555h	2AAh	555h	555h	2AAh	555h
	DATA	AAh	55h	80h	AAh	55h	10h

WA: WRITE ADDRESS  
WD: WRITE DATA  
SA: SECTOR ADDRESS

FIG. 6

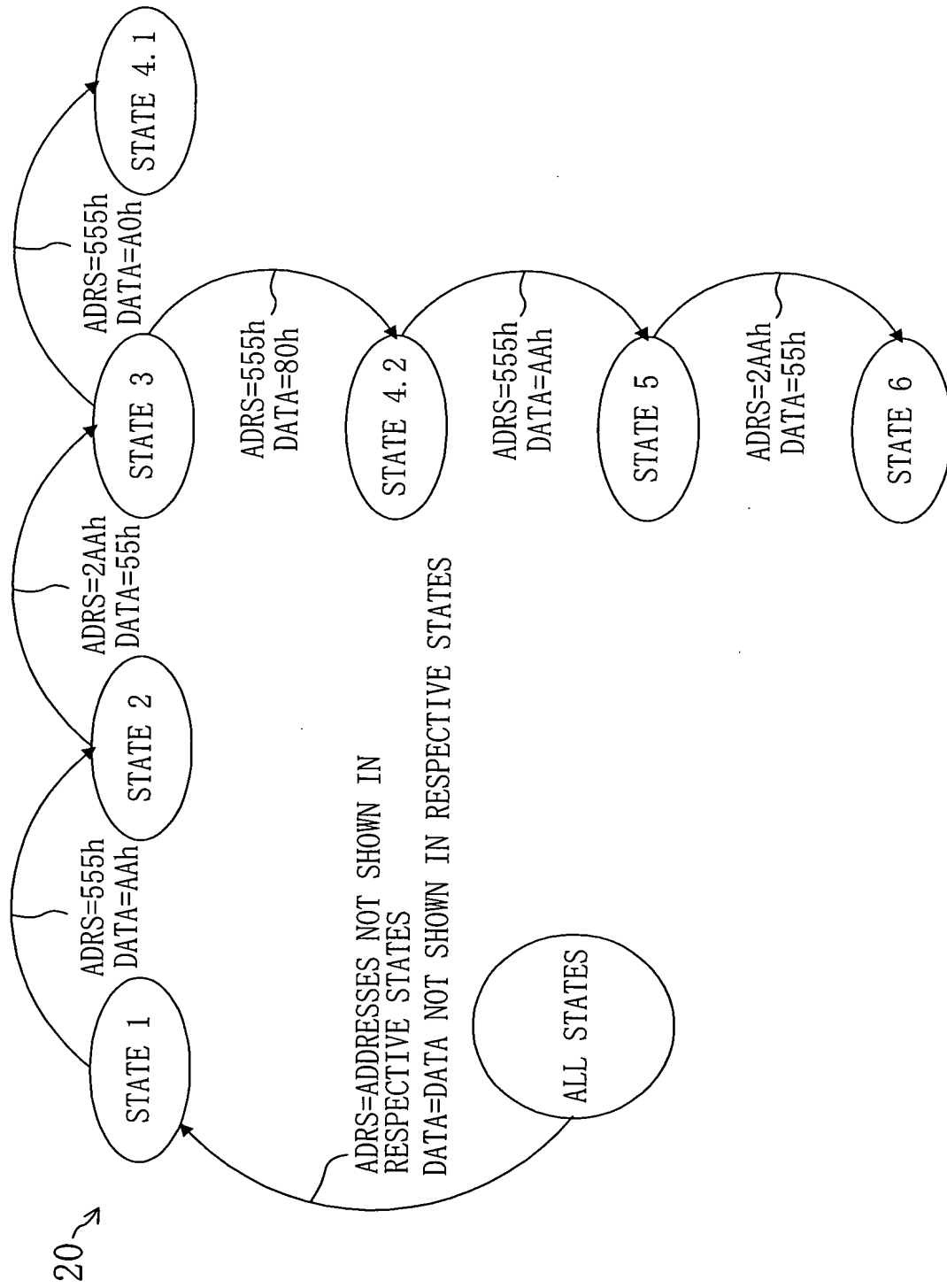


FIG. 7

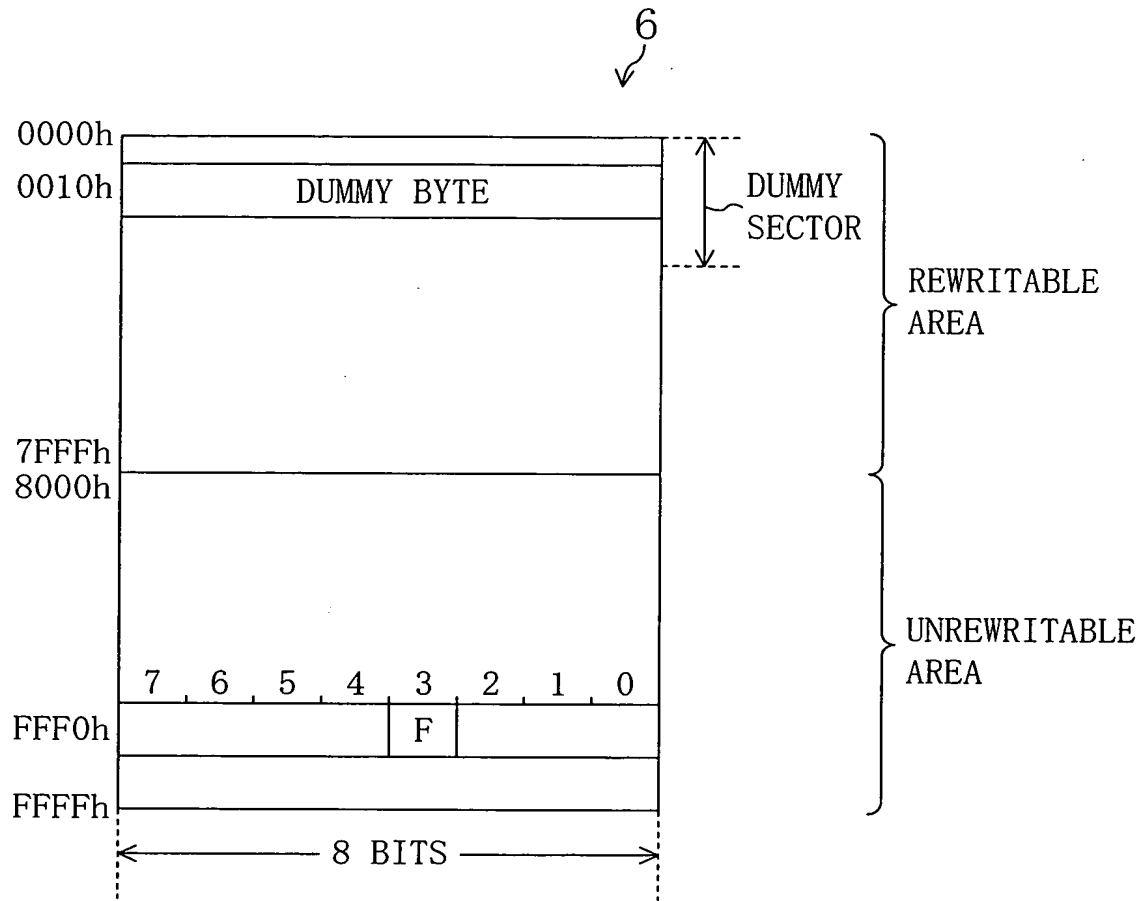


FIG. 8

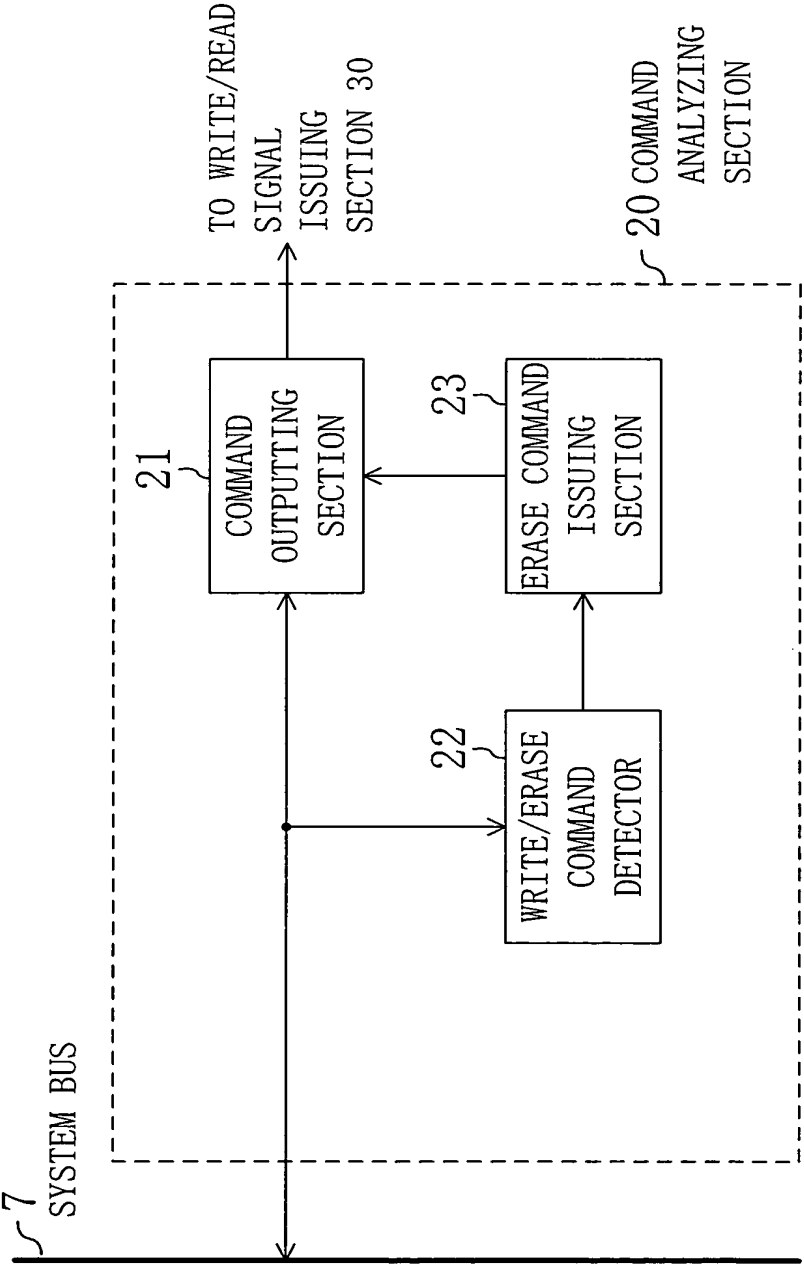




FIG. 9

